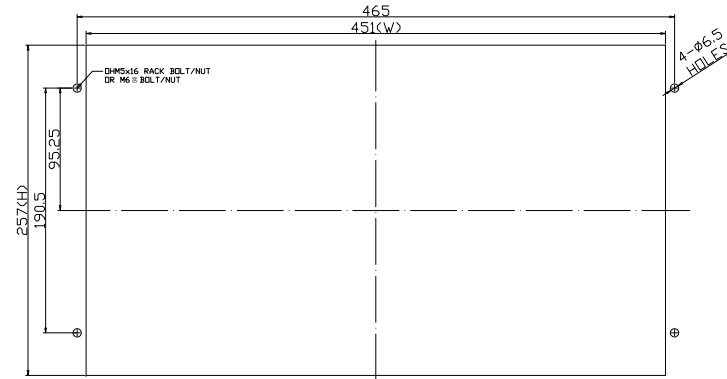
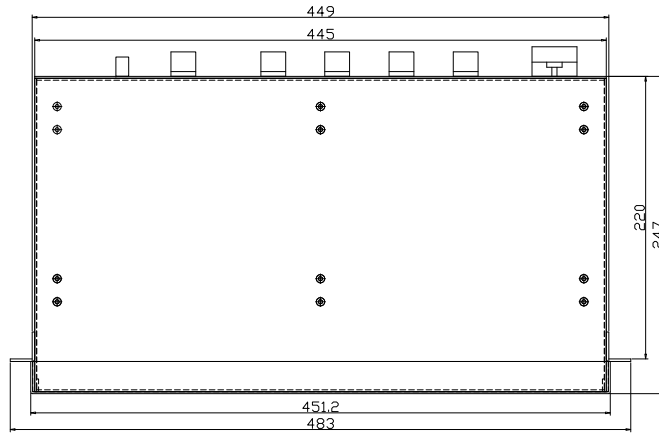
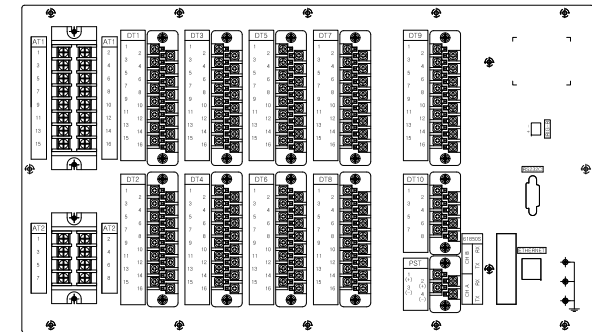
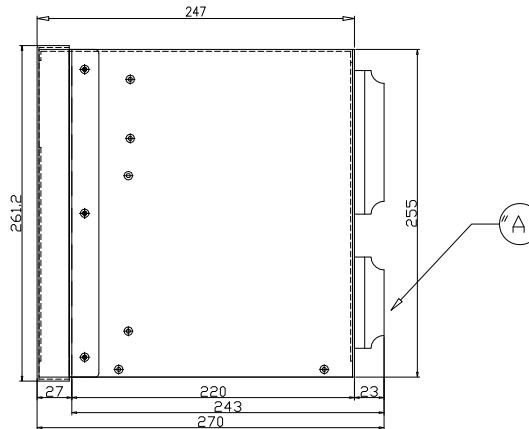
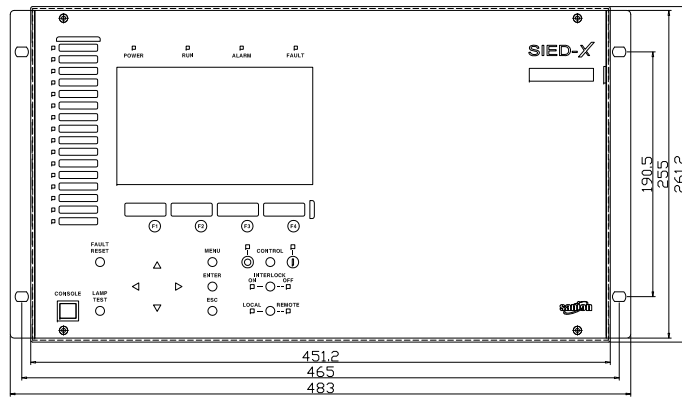
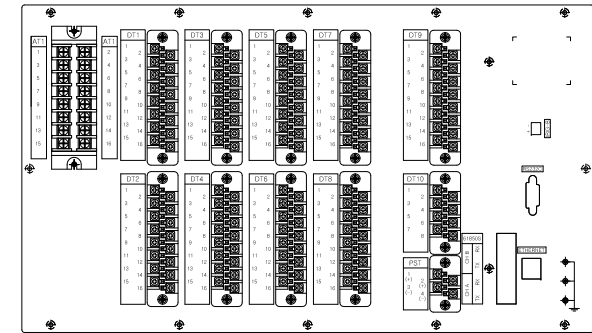




[판넬 가공도]



NOTE : SOC-K110,STC-K110-M IED단자 구성



NOTE : SUF-K110 IED단자 구성

NOTE

- 1.<SOC-K110,STC-K110-M> 과 <SUF-K110> 후면 단자 구성은 상이 함
- 2.저주파수보호 IED(SUF-K110)는 "A" 단자 없음

General	1 ~ 10 : ±0.05	11 ~ 50 : ±0.10
Tolerance	51~200 : ±0.15	201~ : ±0.20

sanion 주식회사 세니온 www.sanion.com			설 계	작 성	검 토	승 인
PROJECT	3rd	PRODUCT NAME	IED		DATE	
UNIT	mm	MODEL NAME	IED-기구-WIDE		2013.07.24	
SCALE	1:1	DWG. NAME	외형도		Rev.	
SIZE	B0	DWG. NO.	DWG-IED-기구-WIDE-0004		1.02	